

1. Memory system and memory subsystem

Inventor: KUZMENKA MAKSIM [DE]
QIMONDA AG [DE]
DE60221407 (T2) — 2008-08-07

2. Circuit and method for adjusting a voltage drop

Inventor: KUZMENKA MAKSIM [DE]
QIMONDA AG [DE]
US2008143427 (A1) — 2008-06-19

3. Connector

Inventor: KUZMENKA MAKSIM [DE]
QIMONDA AG [DE]
DE60316553 (T2) — 2008-04-30

4. Circuit arrangement and method for converting logic signal levels and use of the circuit arrangement

Inventor: KUZMENKA MAKSIM [DE] ; NYGREN AARON [DE]
QIMONDA AG
US2008054940 (A1) — 2008-03-06

5. Microstrip line and method for producing of a microstrip line

Inventor: KUZMENKA MAKSIM [DE]
QIMONDA AG [DE]
DE60313316 (T2) — 2007-12-20

6. Connector for a plurality of switching assemblies with compatible interfaces

Inventor: RUCKERBAUER HERMANN ; KUZMENKA MAKSIM
INFINEON TECHNOLOGIES AG
SG129249 (A1) — 2007-02-26

7. Circuit system and method for coupling a circuit module to or for decoupling same from a main bus

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE60307834 (T2) — 2007-09-13

8. Method, adapter card and arrangement for the installation of memory modules

Inventor: RUCKERBAUER HERMANN ; KUZMENKA MAKSIM
INFINEON TECHNOLOGIES AG
SG127695 (A1) — 2006-12-29

9. Integrated circuit power supply circuit

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE60308703 (T2) — 2007-08-23

10. Topology for providing clock signals to multiple circuit units on a circuit module

Inventor: KUZMENKA MAKSIM [DE] ; CHENNUPATI SIVA RAGHURAM
INFINEON TECHNOLOGIES AG [DE]
DE60308637 (T2) — 2007-08-09

11. Memory device with several random-access memory chip
Inventor: KUZMENKA MAKSIM [DE]; HAUSMANN MICHAEL [DE]
INFINEON TECHNOLOGIES AG [DE]
CN1918660 (A) — 2007-02-21

12. CONFIGURATION, PLUG-IN MOUNT AND CONTACT ELEMENT FOR FIXING AND CONTACTING SWITCHING ASSEMBLIES ON SUBSTRATE
Inventor: RUCKERBAUER HERMANN ; KUZMENKA MAKSIM
INFINEON TECHNOLOGIES AG
KR20040002777 (A) — 2004-01-07

13. Circuit system
Inventor: RUCKERBAUER HERMANN; KUZMENKA MAKSIM ; MUFF SIMON [DE]
INFINEON TECHNOLOGIES AG [DE]
CN1860684 (A) — 2006-11-08

14. Memory system
Inventor: KUZMENKA MAKSIM [DE] ; KOROTKOV KONSTANTIN [DE]
INFINEON TECHNOLOGIES AG [DE]
DE60210170 (T2) — 2006-11-02

15. Memory module and memory configuration with stub-free signal lines and distributed capacitive loads
Inventor: BRAUN GEORG [DE] ; RUCKERBAUER HERMANN [DE]; KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG
US2006202328 (A1) — 2006-09-14

16. Esd protection apparatus for an electrical device
Inventor: MAKSIM KUZMENKA [DE]
INFINEON TECHNOLOGIES AG [DE]
CN1828893 (A) — 2006-09-06

17. A method of transmitting an electrical signal and output driver circuit for transmitting electrical signal.
Inventor: KUZMENKA MAKSIM [DE]; NYGREN AARON [DE]
INFINEON TECHNOLOGIES AG [DE]
CN1822141 (A) — 2006-08-23

18. Driver circuit
Inventor: KUZMENKA MAKSIM; BROX MARTIN [DE]
INFINEON TECHNOLOGIES AG [DE]
CN1773861 (A) — 2006-05-17

19. Circuit and method for transmitting a signal
Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG
US2006116086 (A1) — 2006-06-01

20. Circuit

Inventor: BRAUN GEORG [DE] ; KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG
US2006092715 (A1) — 2006-05-04

21. Circuit board and method for producing a circuit board

Inventor: MAKSIM KUZMENKA [DE]
INFINEON TECHNOLOGIES AG [DE]
CN1741708 (A) — 2006-03-01

22. Circuit board and method for producing a circuit board

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE102005037392 (A1) — 2006-03-30

23. Zipper connector

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE60203605 (T2) — 2006-03-09

24. Apparatus and method for transmitting signals

Inventor: KUZMENKA MAKSIM [DE]
US2005259826 (A1) — 2005-11-24

25. Circuit module

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE102005018640 (A1) — 2005-11-24

26. Circuit board

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE102005017331 (A1) — 2005-11-17

27. CIRCUIT BOARD FOR CONNECTING AN INTEGRATED CIRCUIT TO A SUPPORT AND IC BGA PACKAGE

Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [DE]
DE102005013270 (A1) — 2005-10-27

28. FBGA arrangement

Inventor: THOMAS JOCHEN [DE] ; GRAFE JUERGEN [DE] THOMAS JOCHEN;
KUZMENKA MAKSIM [DE]
US2005098870 (A1) — 2005-05-12

29. Semiconductor memory module

Inventor: RUCKERBAUER HERMANN [DE] ; KUZMENKA MAKSIM [DE]
US2005078532 (A1) — 2005-04-14

30. Semiconductor memory module
Inventor: JAKOBS ANDREAS [DE] ; RUCKERBAUER HERMANN [DE] KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [US]
US2005036349 (A1) — 2005-02-17
31. Semiconductor memory module
Inventor: JAKOBS ANDREAS [DE]; RUCKERBAUER HERMANN [DE] KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [US]
US2005044305 (A1) — 2005-02-24
32. Semiconductor memory module
Inventor: JAKOBS ANDREAS [DE] ; RUCKERBAUER HERMANN [DE] KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG [US]
US2005024963 (A1) — 2005-02-03
33. Error detection in a circuit module
Inventor: KUZMENKA MAKSIM [DE] ; RAGHURMA SIVA [DE]
US2005022074 (A1) — 2005-01-27
34. Memory arrangement
Inventor: BRAUN GEORG [DE] ; RUCKERBAUER HERMANN [DE] BRAUN GEORG; KUZMENKA MAKSIM [DE]
US2005038966 (A1) — 2005-02-17
35. Input receiver circuit
Inventor: KUZMENKA MAKSIM [DE] ; RUCKERBAUER HERMANN [DE]
US2004260964 (A1) — 2004-12-23
36. Memory module having space-saving arrangement of memory chips and memory chip therefore
Inventor: KUZMENKA MAKSIM [DE]
INFINEON TECHNOLOGIES AG
US2004212070 (A1) — 2004-10-28
37. Device and method for converting an input signal
Inventor: KUZMENKA MAKSIM [DE] ; KOROTKOV KONSTANTIN [DE]
US2004189349 (A1) — 2004-09-30
38. Buffer amplifier architecture for semiconductor memory circuits
Inventor: KUZMENKA MAKSIM [DE] ; KIEHL OLIVER [US]
US2004202027 (A1) — 2004-10-14
39. Branched command/address bus architecture for registered memory units
Inventor: KUZMENKA MAKSIM [DE] ; CHENNUPATI SIVA RAGHURAM
US2003131211 (A1) — 2003-07-10

40. Device for supplying control signals to memory units, and a memory unit adapted thereto

Inventor: KUZMENKA MAKSIM [DE]

INFINEON TECHNOLOGIES AG

US2003101296 (A1) — 2003-05-29

41. Signal distribution to a plurality of circuit units

Inventor: KUZMENKA MAKSIM [DE]

INFINEON TECHNOLOGIES AG

US2003099093 (A1) — 2003-05-29

42. Device for accessing registered circuit units

Inventor: KUZMENKA MAKSIM [DE]

INFINEON TECHNOLOGIES AG

US2003084267 (A1) — 2003-05-01

43. Device and method for clock generation

Inventor: KUZMENKA MAKSIM [DE]

INFINEON TECHNOLOGIES AG

US2003080794 (A1) — 2003-05-01

44. Interface connection method for transmitting/receiving data links to two lines in a pair of lines regarded as a single line in order to transmit data by means of differential signals

Inventor: KUZMENKA MAKSIM [DE]

INFINEON TECHNOLOGIES AG [DE]

DE10234720 (A1) — 2004-02-12

45. Steckfassung mit im unbestückten Zustand kurzgeschlossenen Kontaktelementen

Inventor: RUCKERBAUER HERMANN [DE] ; KUZMENKA MAKSIM [DE]

INFINEON TECHNOLOGIES AG [DE]

DE10229170 (A1) — 2004-01-29